

**UTTARA UNIVERSITY**  
**Semester Final Exam Schedule**  
**Department of Computer Science & Engineering**  
**Semester: Fall - 2016**

		Saturday 17/12/2016			Saturday 17/12/2016			Saturday 17/12/2016		
Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Saturday 17/12/2016	06:00 PM - 08:00 PM	MCSE SP2	Digital Signal Processing	MSc 36,37,38	MEH	502	1,3,5	ALL		
		MNGT 228	Management & Organization Behavior	BSc 34 Dip	MDH	601	1,3,5	ALL		
		CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 36A Dip	SHC	601	2,4,6	1-19		
						602	2,4,6	20-36		
		MNGT 228	Management & Organization Behavior	BSc 33 Dip	SHK	602	1,3,5	ALL		
		MATH 221	Linear Algebra & Fourier Transform	BSc 37A Dip	AMU	603	1,3,5	1-20		
						604	1,3,5	21-40		
		CSEC 325	Compiler Construction	BSc 35 Dip	CSI	603	2,4,6	1-25		
						604	2,4,6	26-50		
		CSEC 229	Computer Architecture	BSc 38B Dip	MHZ	301	1,3,5	56-84		
						304	1,3,5	85-108		
		MATH 221	Linear Algebra & Fourier Transform	BSc 37B Dip	AMU	301	2,4,6	39-68		
						304	2,4,6	69-93		
		CSEC 229	Computer Architecture	BSc 38A Dip	MHZ	701	1,3,5	1-25		
				702	1,3,5	26-55				
CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 36B Dip	MTH	701	2,4,6	37-55				
				702	2,4,6	56-73				

**Monday 19/12/2016****Monday 19/12/2016****Monday 19/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
<b>Monday 19/12/2016</b>	<b>02:00 PM – 4:00 PM</b>	<b>MATH 123</b>	<b>Discrete Mathematics</b>	<b>BSc 39 Reg</b>	<b>ALD</b>	<b>601</b>	<b>1,3,5</b>	<b>1-23</b>	
						<b>602</b>	<b>1,3,5</b>	<b>24-50</b>	
		<b>CSEC 111</b>	<b>Computer System &amp; Application</b>	<b>BSc 40 Reg</b>	<b>SAC</b>	<b>601</b>	<b>2,4,6</b>	<b>1-17</b>	
						<b>602</b>	<b>2,4,6</b>	<b>18-33</b>	
		<b>ACTG 121</b>	<b>Accounting</b>	<b>BSc 38C Reg</b>	<b>ASA</b>	<b>603</b>	<b>1,3,5</b>	<b>81-103</b>	
						<b>604</b>	<b>1,3,5</b>	<b>104-127</b>	
	<b>ACTG 121</b>	<b>Accounting</b>	<b>BSc 38B Reg</b>	<b>ASA</b>	<b>603</b>	<b>2,4,6</b>	<b>41-63</b>		
					<b>604</b>	<b>2,4,6</b>	<b>64-80</b>		
	<b>ACTG 121</b>	<b>Accounting</b>	<b>BSc 38 A Reg</b>	<b>ARI</b>	<b>302</b>	<b>2,4,6</b>	<b>1-25</b>		
					<b>303</b>	<b>2,4,6</b>	<b>26-40</b>		
	<b>ECON 229</b>	<b>Economics</b>	<b>BSc 36 Reg</b>	<b>ARI</b>	<b>302</b>	<b>1,3,5</b>	<b>1-30</b>		
					<b>303</b>	<b>1,3,5</b>	<b>31-61</b>		
	<b>CSEC 324</b>	<b>Measurement &amp; Instrumentation</b>	<b>BSc 34 Reg</b>	<b>NDH</b>	<b>301</b>	<b>1,3,5</b>	<b>1-34</b>		
					<b>304</b>	<b>1,3,5</b>	<b>35-73</b>		
	<b>CSEC 432</b>	<b>Computer Communication &amp; Networks</b>	<b>BSc 32 Reg</b>	<b>DMS</b>	<b>301</b>	<b>2,4,6</b>	<b>1-27</b>		
					<b>304</b>	<b>2,4,6</b>	<b>28-55</b>		
	<b>CSEC 462</b>	<b>Digital Signal Processing Sessional</b>	<b>BSc 31 Reg</b>	<b>MEH</b>	<b>LAB 1</b>	<b>ALL</b>	<b>ALL</b>	<b>MEH, Galib</b>	
	<b>06:00 PM – 08:00 PM</b>	<b>MATH 123</b>	<b>Discrete Mathematics</b>	<b>BSc 39A Dip</b>	<b>ALD</b>	<b>601</b>	<b>1,3,5</b>	<b>1-22</b>	
<b>602</b>						<b>1,3,5</b>	<b>23-50</b>		
		<b>Communication Skill</b>	<b>BSc 40 A Dip</b>	<b>IWS</b>	<b>601</b>	<b>2,4,6</b>	<b>1-23</b>		
					<b>602</b>	<b>2,4,6</b>	<b>24-45</b>		

Monday 19/12/2016	06:00 PM	ENG 100	Communication Skill	BSc 40 B Dip	IWS	603	2,4,6	46-68		
	-	08:00 PM	MATH 123	Discrete Mathematics	BSc 39B Dip	MTR	604	2,4,6		69-90
							603	1,3,5		51-71
							604	1,3,5		72-100

**Tuesday 20/12/2016**

**Tuesday 20/12/2016**

**Tuesday 20/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Tuesday 20/12/2016	11:00 AM	CSEC 112	Computer System & Application Sessional	BSc 40 Reg	SAC	LAB 1	ALL	ALL	Kaiser, Galib	
	-	01:00 PM	CSEC 228	Operating System Design Sessional	BSc 34 Reg	ALY	LAB 2	ALL	ALL	ALY, Parveg
		CSEC 218	Digital Logic Design Sessional	BSc 37 Reg	SHC	LAB 3 & 303	ALL	ALL	SHC, Rubel	
		EENG 122	Electrical Circuit Sessional	BSc 38A Reg	MTH	LAB 5 & 502	ALL	ALL	MTH, Shamim	
	02:00 PM	EENG 122	Electrical Circuit Sessional	BSc 38B Reg	MHS	LAB 5 & 502	ALL	ALL	MHS, Shamim	
	-	04:00 PM	CSEC 433	Computer Communication & Networks Sessional	BSc 32 Reg	DMS	LAB 1	ALL	ALL	DMS, Galib

**Wednesday 21/12/2016**

**Wednesday 21/12/2016**

**Wednesday 21/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Wednesday 21/12/2016	11:00 AM	CSEC 217	Digital Logic Design	BSc 37 Reg	SHC	604	3,5	ALL		
	-	01:00 PM	MATH 462	Numerical Analysis & Coordinate Geometry	BSc 30 Reg	AMU	603	1,3,5		ALL
					BSc 31 Reg		604	1,3		ALL
					CSEC 229	Computer Architecture	BSc 35A Reg	ALY		603
	CSEC 325	Compiler Construction	BSc 32 Reg	CSI	601	1,3,5	1-27			
					602	1,3,5	28-55			

Wednesday 21/12/2016	11:00 AM -	CSEC 229	Computer Architecture	BSc 35B Reg	MAK	601	2,4,6	51-77	
						602	2,4,6	78-100	
	01:00 PM	CSEC 227	Operating System Design	BSc 34 Reg	ALY	302	1,3,5	1-34	
						303	1,3,5	35-73	
	CSEC 321	Database Management System	BSc 33 Reg	SHS	302	2,4,6	1-34		
					303	2,4,6	35-59		

**Thursday 22/12/2016**

**Thursday 22/12/2016**

**Thursday 22/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Thursday 22/12/2016	11:00 AM -	CSEC 322	Database Management System Sessional	BSc 33 Reg	SHS	LAB 1	ALL	ALL	Kaiser, Galib
		CSEC 319	Computer Peripheral, Interfacing & Maintenance Sessional	BSc 35 A Reg	TZW	LAB 2	ALL	ALL	TZW, Parveg
		CSEC 326	Compiler Construction Sessional	BSc 32 Reg	CSI	LAB 7	ALL	ALL	CSI, Rubel
		EENG 122	Electrical Circuit Sessional	BSc 38C Reg	ABS	LAB 5 & 502	Electrical Circuit	BSc 38C Reg	ABS, Shamim
		ENG 100	Communication Skill	BSc 40 Reg	IWS	603	2,4,6	1-17	
						604	2,4,6	18-33	
		MATH 129	Complex Variable & Laplace Transform	BSc 38C Reg	AMU	603	1,3,5	81-103	
						604	1,3,5	104-127	
		CSEC 121	Programming Language & Application I	BSc 39 Reg	RHR	601	2,4,6	1-23	
						602	2,4,6	24-50	
		MATH 129	Complex Variable & Laplace Transform	BSc 38B Reg	AMU	601	1,3,5	41-63	
						602	1,3,5	64-80	
		CSEC 229	Computer Architecture	BSc 36 Reg	MHZ	302	1,3,5	1-30	
						303	1,3,5	31-61	
MATH 129	Complex Variable & Laplace Transform	BSc 38A Reg	AMU	302	2,4,6	1-25			
				303	2,4,6	26-40			

Thursday 22/12/2016	02:00 PM - 04:00 PM	CSEC 319	Computer Peripheral, Interfacing & Maintenance Sessional	BSc 35 B Reg	TZW	LAB 1	ALL	ALL	TZW, Galib
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**Friday 23/12/2016**

**Friday 23/12/2016**

**Friday 23/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Friday 23/12/2016	08:30 AM - 10:30 AM	CSEC 111	Computer System & Application	BSc 40A Dip	MMR	603	2,4,6	1-23		
						604	2,4,6	24-45		
			CSEC 316	System Analysis & Design	BSc 36B Dip	MKH	603	1,3,5	37-55	
							604	1,3,5	56-73	
			CSEC 316	System Analysis & Design	BSc 36A Dip	MKH	601	2,4,6	1-19	
							602	2,4,6	37-55	
			CSEC 217	Digital Logic Design	BSc 38B Dip	MHS	601	1,3,5	56-84	
							602	1,3,5	85-108	
			CSEC 213	Basic Electronics	B.Sc. 39A Dip	AUI	301	1,3,5	1-22	
							304	1,3,5	23-50	
			CSEC 217	Digital Logic Design	BSc 38A Dip	MHS	301	2,4,6	1-25	
							304	2,4,6	26-55	
			CSEC 213	Basic Electronics	B.Sc. 39B Dip	AUI	701	1,3,5	51-71	
							702	1,3,5	72-100	
			CSEC 111	Computer System & Application	BSc 40B Dip	MEH	701	2,4,6	46-68	
							702	2,4,6	69-90	
	10:50 AM - 12:50 AM	CSEC 227	Operating System Design	BSc 37B Dip	ALY	601	2,4,6	39-68		
						602	2,4,6	69-93		
			CSEC 432	Computer Communication & Networks	BSc 33 Dip	TSI	601	1,3,5	ALL	
			CSEC 422	Internet Programming & Sessional (Theory Part)	BSc 34 Dip	RHR	602	1,3,5	ALL	

Friday 23/12/2016	10:50 AM -	CSEC 227	Operating System Design	BSc 37A Dip	SRS	603	1,3,5	1-20		
						604	1,3,5	21-38		
	12:50 AM	CSEC 318	Computer Peripheral, Interfacing & Maintenance	BSc 35 Dip	TZW	603	2,4,6	1-25		
						604	2,4,6	26-50		
		CSEC 112	Computer System & Application Sessional	BSc 40A Dip	MMR	LAB 2	ALL	ALL	MMR, Parveg	
		CSEC 317	System Analysis & Design Sessional	BSc 36A Dip	MKH	304	ALL	ALL	MKH, Rubel	
		CSEC 112	Computer System & Application Sessional	BSc 40B Dip	MEH	LAB 1	ALL	ALL	MEH, Kaiser	
		CSEC 218	Digital Logic Design Sessional	BSc 38A Dip	MHS	LAB 3 & 301	ALL	ALL	MHS, Rubel	
		CSEC 214	Basic Electronics Sessional	B.Sc. 39B Dip	AUI	LAB 5 & 502	ALL	ALL	AUI, Shamim	
	02:30 PM -	04:30 PM	CSEC 319	Computer Peripheral, Interfacing & Maintenance Sessional	BSc 35 Dip	TZW	LAB 1	ALL	ALL	TZW, Galib
			CSEC 317	System Analysis & Design Sessional	BSc 36B Dip	MKH	604	ALL	ALL	MKH
			CSEC 433	Computer Communication & Networks Sessional	BSc 33 Dip	TSI	LAB 7	1,3,5	ALL	TSI, Kaiser
			CSEC 422	Internet Programming & Sessional (Sessional Part)	BSc 34 Dip	RHR	LAB 2	2,4,6	ALL	RHR, Parveg
			CSEC 218	Digital Logic Design Sessional	BSc 38B Dip	MHS	LAB 3 & 301	ALL	ALL	MHS, Shamim
			CSEC 214	Basic Electronics Sessional	B.Sc. 39A Dip	AUI	LAB 5 & 502	ALL	ALL	AUI, Rubel
MCSE 103			Computer Organization	MSc 36,37,38	SHK	603	1,2,3	ALL		
04:50 PM -	06:50 PM	CSEC 228	Operating System Design Sessional	BSc 37A Dip	SRS	LAB 1	ALL	ALL	SRS, Galib	
		CSEC 228	Operating System Design Sessional	BSc 37B Dip	ALY	LAB2	ALL	ALL	ALY, Parveg	



<b>Saturday 24/12/2016</b>	<b>06:00 PM – 08:00 PM</b>	<b>CSEC 226</b>	<b>Microprocessor Interfacing &amp; Low Level Language Sessional</b>	<b>BSc 36A Dip</b>	<b>SHC</b>	<b>LAB 1</b>	<b>ALL</b>	<b>ALL</b>	<b>SHC</b>
		<b>CSEC 226</b>	<b>Microprocessor Interfacing &amp; Low Level Language Sessional</b>	<b>BSc 36B Dip</b>	<b>MTH</b>	<b>LAB 2</b>	<b>ALL</b>	<b>ALL</b>	<b>MTH</b>
		<b>CSEC 326</b>	<b>Compiler Construction Sessional</b>	<b>BSc 35 Dip</b>	<b>CSI</b>	<b>LAB 1</b>	<b>ALL</b>	<b>ALL</b>	<b>CSI, KAISER</b>
		<b>MCSE 101</b>	<b>Microcomputer &amp; Microprocessor</b>	<b>MSc 36,37,38</b>	<b>SHK</b>	<b>502</b>	<b>1,2</b>	<b>ALL</b>	
		<b>MATH 110</b>	<b>Differential &amp; Integral Calculus</b>	<b>BSc 40B Dip</b>	<b>TSI</b>	<b>603</b>	<b>1,3,5</b>	<b>46-68</b>	
						<b>604</b>	<b>1,3,5</b>	<b>69-90</b>	
		<b>MATH 110</b>	<b>Differential &amp; Integral Calculus</b>	<b>BSc 40A Dip</b>	<b>TSI</b>	<b>603</b>	<b>2,4,6</b>	<b>1-23</b>	
						<b>604</b>	<b>2,4,6</b>	<b>24-45</b>	
<b>MATH 129</b>	<b>Complex Variable &amp; Laplace Transform</b>	<b>BSc 34 Dip</b>	<b>AMU</b>	<b>601</b>	<b>1,3,5,6</b>	<b>ALL</b>			

**MONDAY 26/12/2016**

**MONDAY 26/12/2016**

**MONDAY 26/12/2016**

<b>Day</b>	<b>Time</b>	<b>Course Code</b>	<b>Course Title</b>	<b>Batch</b>	<b>Teacher</b>	<b>Room No</b>	<b>Column No</b>	<b>Student ID</b>	<b>Invigilator</b>
<b>Monday 26/11/2016</b>	<b>02:00 AM – 04:00 PM</b>	<b>CSEC 122</b>	<b>Programming Language &amp; Application I Sessional</b>	<b>BSc 39 Reg</b>	<b>RHR</b>	<b>LAB 1</b>	<b>ALL</b>	<b>ALL</b>	<b>RHR, Galib</b>
		<b>CSEC 212</b>	<b>Programming Language &amp; Application II Sessional</b>	<b>BSc 38 C Reg</b>	<b>SRS</b>	<b>LAB 2</b>	<b>ALL</b>	<b>ALL</b>	<b>SRS, Parveg</b>
		<b>CSEC 213</b>	<b>Basic Electronics</b>	<b>B.Sc. 37 Reg</b>	<b>AUI</b>	<b>603</b>	<b>1,3</b>	<b>ALL</b>	
		<b>CSEC 428</b>	<b>Computer Graphics</b>	<b>BSc. 31 Reg</b>	<b>MSH</b>	<b>603</b>	<b>3,5</b>	<b>ALL</b>	
		<b>CSEC 422</b>	<b>Internet Programming &amp; Sessional (Theory Part)</b>	<b>BSc 30 Reg</b>	<b>MSH</b>	<b>604</b>	<b>1,3,5</b>	<b>ALL</b>	
		<b>CSEC 313</b>	<b>Object Oriented Programming</b>	<b>BSc 35A Reg</b>	<b>UKD</b>	<b>603</b>	<b>2,4,6</b>	<b>1-23</b>	
						<b>604</b>	<b>2,4,6</b>	<b>24-50</b>	
<b>CSEC 428</b>	<b>Computer Graphics</b>	<b>BSc. 33 Reg</b>	<b>MSH</b>	<b>502</b>	<b>1,3,5</b>	<b>ALL</b>			



Monday 26/11/2016	02:00 AM – 04:00 PM	CSEC 313	Object Oriented Programming	BSc 35B Reg	UKD	302	2,4,6	51-77	
						303	2,4,6	78-100	
	CSEC 321	Database Management System	BSc 34 Reg	TSI	302	1,3,5	1-34		
					303	1,3,5	35-73		

**TUESDAY 27/12/2016**

**TUESDAY 27/12/2016**

**TUESDAY 26/12/2016**

Day	Time	Course	Course Title	Batch	Teacher	Room	Column No	Student	Invigilator
Tuesday 27/12/2016	11:00 AM – 01:00 PM	CSEC 429	Computer Graphics Sessional	BSc. 31 Reg	MSH	LAB 1	ALL	ALL	MSH, Galib
				BSc. 33 Reg					
Tuesday 27/12/2016	01:00 PM	CSEC 422	Internet Programming & Sessional (Sessional Part)	BSc 30 Reg	MSH	LAB 7	ALL	ALL	MSH, Rubel
		CSEC 314	Object Oriented Programming Sessional	BSc 35A Reg	UKD	LAB 2	ALL	ALL	UKD, Parbeg
	02:00 PM – 04:00 PM	CSEC 214	Basic Electronics Sessional	B.Sc. 37 Reg	AUI	502 & LAB 5	ALL	ALL	AUI, Shamim
		CSEC 314	Object Oriented Programming Sessional	BSc 35B Reg	UKD	LAB 2	ALL	ALL	UKD, Parbeg
	CSEC 322	Database Management System Sessional	BSc 34 Reg	TSI	LAB 1	ALL	ALL	TSI, Galib	

**WEDNESDAY 28/12/2016**

**WEDNESDAY 28/12/2016**

**WEDNESDAY 28/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Wednesday 28/12/2016	11:00 AM – 01:00 PM	MATH 110	Differential & Integral Calculus	BSc 40 Reg	MBH	603	2,4,6	1-17	
						604	2,4,6	18-33	
		CSEC 211	Programming Language & Application II	BSc 38C Reg	SRS	603	1,3,5	81-103	
						604	1,3,5	104-127	
Wednesday	11:00 AM	CSEC 211	Programming Language & Application II	BSc 38A Reg	SHS	302	2,4,6	1-25	
						303	2,4,6	26-40	
		CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 36 Reg	NDH	302	1,3,5	1-30	
						303	1,3,5	31-61	

28/12/2016	– 01:00 PM	CSEC 211	Programing Language & Application II	BSc 38B Reg	MMR	601	1,3,5	41-63	
		EENG 121	Electrical Circuit	BSc 39 Reg	ABS	602	1,3,5	64-80	
						601	2,4,6	1-23	
						602	2,4,6	24-50	

**THURSDAY 29/12/2016**

**THURSDAY 29/12/2016**

**THURSDAY 29/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Thursday 29/12/2016	11:00 AM – 01:00 PM	CSEC 212	Programing Language & Application II Sessional	BSc 38 A Reg	SHS	LAB 1	ALL	ALL	SHS, Galib
		CSEC 216	Data Structure Sessional	BSc 37 Reg	MEH	LAB 7	ALL	ALL	MEH,Rebel
		EENG 122	Electrical Circuit Sessional	BSc 39 Reg	ABS	LAB 5 & 502	ALL	ALL	ABS, Shamim
		CSEC 226	Microprocessor Interfacing & Low Level Language Sessional	BSc 36 Reg	NDH	LAB 2	ALL	ALL	NDH, Shamim
	02:00 PM – 04:00 PM	CSEC 212	Programing Language & Application II Sessional	BSc 38 B Reg	MMR	LAB 2	ALL	ALL	MMR, Parveg

**FRIDAY 30/12/2016**

**FRIDAY 30/12/2016**

**FRIDAY 30/12/2016**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Friday 30/12/2016	08:30 AM - 10:30 AM	CSEC 226	Microprocessor Interfacing & Low Level Language Sessional	BSc 37A Dip	NDH	LAB 1	ALL	ALL	NDH, Kaiser
		CSEC 226	Microprocessor Interfacing & Low Level Language Sessional	BSc 37B Dip	SHC	LAB 2	ALL	ALL	SHC, MHS
		CSEC 121	Programming Language & Application I	BSc 40B Dip	MTR	601	2,4,6	46-68	
						602	2,4,6	69-90	
		CSEC 211	Programing Language & Application II	BSc 39B Dip	MHZ	601	1,3,5	51-71	
Friday 30/12/2016		CSEC 121	Programming Language & Application I	BSc 40A Dip	ALD	603	2,4,6	1-23	
						604	2,4,6	24-45	
		CSEC 211	Programing Language & Application II	BSc 39A Dip	MTR	603	1,3,5	1-22	
						604	1,3,5	23-50	

Friday 30/12/2016	10:50 AM - 12:50 AM	CSEC 122	Programming Language & Application I Sessional	BSc 40B Dip	MTR	LAB 7	ALL	ALL	MTR, Rubel
		CSEC 212	Programming Language & Application II Sessional	BSc 39B Dip	MHZ	LAB 1	ALL	ALL	MHZ, Galib
	CSEC 122	Programming Language & Application I Sessional	BSc 40A Dip	ALD	LAB 2	ALL	ALL	ALD, Parveg	
	CSEC 423	VLSI Design & Sessional (Sessional Part)	BSc 33 Dip	ABS	502	ALL	ALL	ABS, Shamim	
			BSc 34 Dip						
	CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 37B Dip	SHC	601	1,3,5	39-68		
					602	1,3,5	69-93		
	CSEC 311	Design & Analysis of Algorithm	BSc 36A Dip	UKD	601	2,4,6	1-19		
					602	2,4,6	20-36		
	CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 37A Dip	NDH	603	1,3,5	1-20		
					604	1,3,5	21-38		
	CSEC 311	Design & Analysis of Algorithm	BSc 36B Dip	SHS	603	1,3,5	37-55		
					604	1,3,5	56-73		
	02:30 PM - 04:30 PM	MCSE 105	Data Communication & Computer Networks with CSM	MSc 36,37,38	CSI	502	1,2	ALL	
		CSEC 212	Programming Language & Application II Sessional	BSc 39A Dip	MTR	LAB 1	ALL	ALL	MTR, Galib
		CSEC 312	Design & Analysis of Algorithm Sessional	BSc 36A Dip	UKD	LAB 2	ALL	ALL	UKD, Parveg
		CSEC 312	Design & Analysis of Algorithm Sessional	BSc 36B Dip	SHS	LAB 7	ALL	ALL	SHS, Rubel
		CSEC 215	Data Structure	BSc 38 A Dip	RHR	601	1,3,5	1-25	
602						1,3,5	26-55		
02:30 PM	CSEC 316	System Analysis & Design	BSc 35 Dip	MKH	601	2,4,6	1-25		
					602	2,4,6	26-50		

	- 04:30 PM	CSEC 215	Data Structure	BSc 38 B Dip	MSH	603	1,3,5	56-84	
						604	1,3,5	85-108	
		CSEC 423	VLSI Design & Sessional (Theory Part)	BSc 33 Dip	ABS	603	1,3,5	ALL	
				BSc 34 Dip		604	1,3,5		
	04:50 PM	CSEC 216	Data Structure Sessional	BSc 38 A Dip	RHR	LAB 1	ALL	ALL	RHR,GALIB
	-	CSEC 216	Data Structure Sessional	BSc 38 B Dip	MSH	LAB 2	ALL	ALL	MSH,Parveg
	06:50 PM	CSEC 317	System Analysis & Design Sessional	BSc 35 Dip	MKH	502	ALL	ALL	MKH, Shamim

### SATURDAY 31/12/2016

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Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Saturday 31/12/2016	06:00 PM - 08:00 PM	STAT 121	Statistics & Probability	BSc 39B Dip	ALY	601	1,3,5	51-71		
						602	1,3,5	72-100		
			MATH 129	Complex Variable & Laplace Transform	BSc 38B Dip	MBH	601	2,4,6	56-84	
							602	2,4,6	85-108	
			CSEC 324	Measurement & Instrumentation	BSc 36A Dip	MTH	603	1,3,5	1-19	
							604	1,3,5	20-36	
			CSEC 323	Data Communication	BSc 37B Dip	SRS	603	1,3,5	39-68	
							604	1,3,5	69-93	
			CSEC 323	Data Communicati1on	BSc 37A Dip	MSH	301	1,3,5	1-20	
							304	1,3,5	21-38	
			CSEC 324	Measurement & Instrumentation	BSc 36B Dip	SHK	301	2,4,6	37-55	
							304	2,4,6	56-73	
			STAT 121	Statistics & Probability	BSc 39A Dip	ALD	701	1,3,5	1-22	
							702	1,3,5	23-50	
		MATH 129	Complex Variable & Laplace Transform	BSc 38A Dip	MBH	701	2,4,6	1-25		
						702	2,4,6	26-55		

<b>Monday 02/01/2017</b>		<b>Monday 02/01/2017</b>			<b>Monday 02/01/2017</b>		
<b>Day</b>	<b>Time</b>	<b>Course Code</b>	<b>Course Title</b>	<b>Batch</b>	<b>Teacher</b>	<b>Room No</b>	<b>Invigilator</b>
<b>Monday 02/01/2017</b>	<b>11:00 AM</b>	<b>CSEC 399</b>	<b>Project I</b>	<b>BSc 30 Reg</b>	<b>ALD</b>	<b>LAB1</b>	<b>ALD, UKD, MMR</b>
	<b>–</b>	<b>CSEC 499</b>	<b>Project II</b>	<b>BSc 29 Reg</b>	<b>RHR</b>	<b>LAB2</b>	<b>RHR, SHS, MHZ</b>
	<b>01:00 PM</b>						
	<b>06:00 PM</b>	<b>CSEC 399</b>	<b>Project I</b>	<b>BSc 32 Dip</b>	<b>ALD</b>	<b>LAB1</b>	<b>ALD, CSI</b>
	<b>–</b>	<b>CSEC 499</b>	<b>Project II</b>	<b>BSc 31 Dip</b>	<b>MMR</b>	<b>LAB2</b>	<b>MMR, MKH</b>
	<b>08:00 PM</b>	<b>MCSE SP 5</b>	<b>Project I</b>	<b>MSc 36</b>	<b>SAC</b>	<b>601</b>	<b>SAC, SHK</b>
		<b>MCSE SP 6</b>	<b>Project II</b>	<b>MSc 37</b>	<b>SHK</b>	<b>601</b>	<b>SAC, SHK</b>
	<b>MCSE SP 7</b>	<b>Thesis</b>	<b>MSc 38</b>	<b>SAC</b>	<b>601</b>	<b>SAC, SHK</b>	

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